FIG. 1 RELATED ART

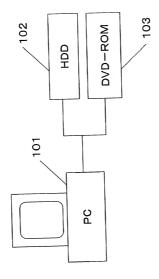


FIG. 2 RELATED ART

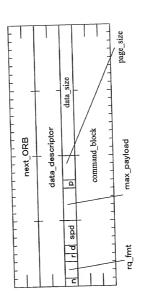


FIG. 3 RELATED ART

Operation_Code(28h = Read10) Reserve	
---	--

FIG. 4 RELATED ART

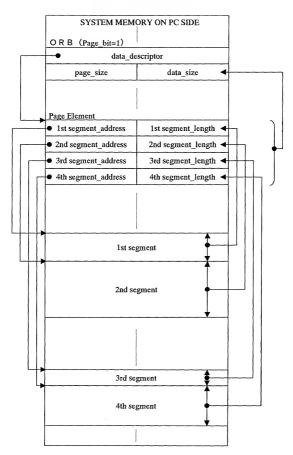
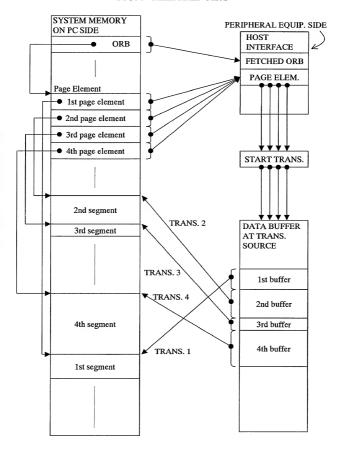
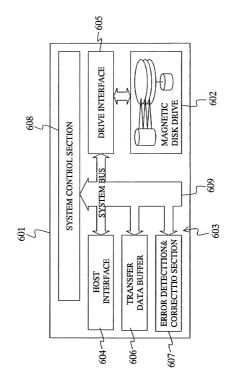


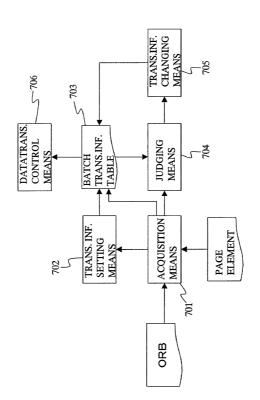
FIG. 5 RELATED ART











PERIPHERAL EQUIP. SIDE

SYSTEM MEMORY

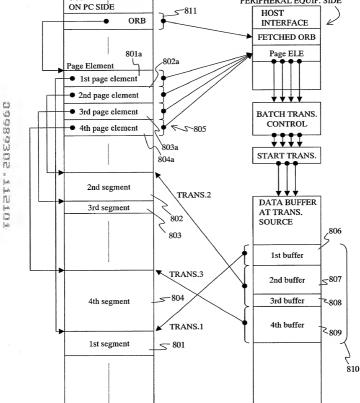


FIG. 9A

703	segment_base_hi	segment_base_lo	buffer_point	transfer length
	page_counter	segment	puffer	transfer

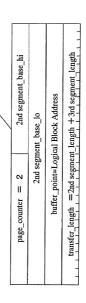
FIG. 9B

703	1st segment_base_hi	1st segment_base_lo	cal Block Address	st segment_length
	page_counter = data_size	1st segm	buffer_point=Logical Block Address	transfer length = 1st segment length

FIG. 10A

703	2nd segment_base_hi	2nd segment_base_lo	cal Block Address	nd segment length
	page_counter = 2	2nd segm	buffer_point=Logical Block Address	transfer_length ==2nd segment_length

FIG. 10B



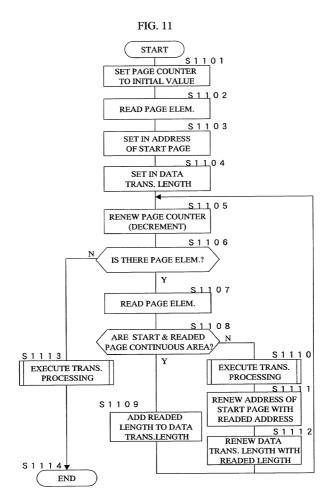
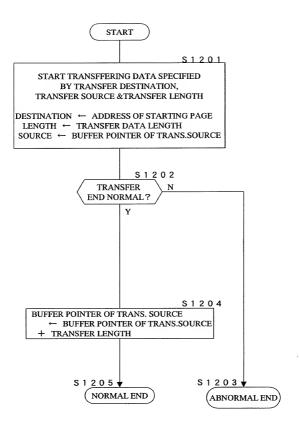
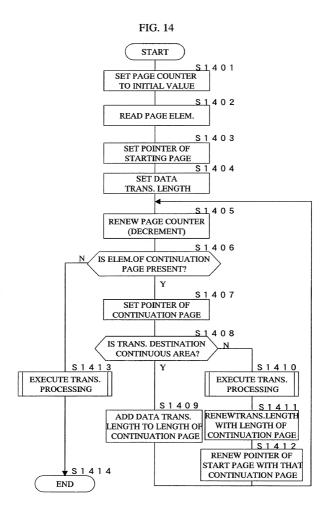


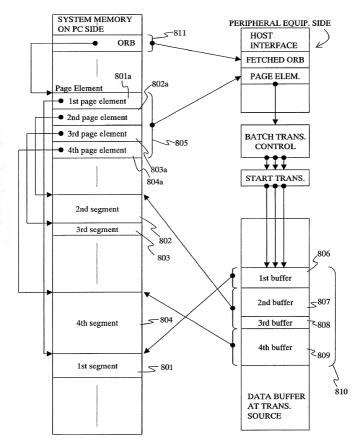
FIG. 12



start_page	end_page	page_counter
	transfer_length	length
	buffer_point	point
1st segn	1st segment_length	1st segment_base_hi
	1st segment_base_lo	t_base_lo
2nd segn	2nd segment_length	2nd segment_base_hi
	2nd segment_base_lo	nt_base_lo
nth segn	nth segment_length	nth segment_base_hi
	nth segment_base_lo	t_base_lo







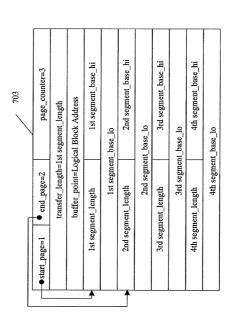


FIG 1

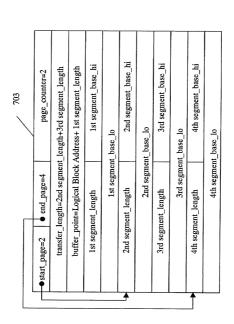
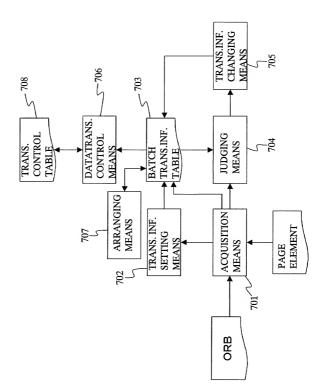
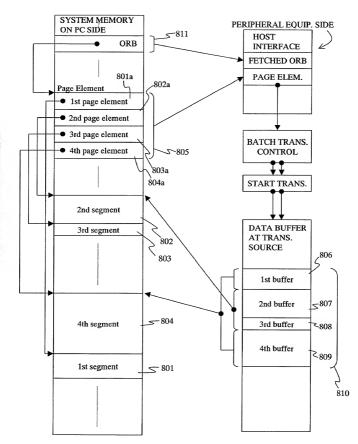


FIG. 18

page_counter	length	1st segment_base_hi	t_base_lo	1st buffer_point	2nd segment_base_hi	nt_base_lo	2nd buffer_point		nth segment_base_hi	ıt_base_lo	nth buffer_point
end_page	transfer_length	1st segment_length	1st segment_base_lo	1st buff	2nd segment_length	2nd segment_base_lo	2nd buff	***************************************	nth segment_length	nth segment_base_lo	nth buff
start_page		1st a			2nd				nth		

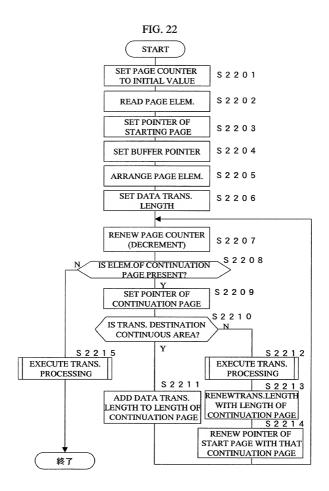
FIG. 19





TOTALL MORESTED

703	page_counter=4	d segment_length	2nd segment_base_hi	ıt_base_lo	er_point	3rd segment_base_hi	ıt_base_lo	r_point	4th segment_base_hi	t_base_lo	r_point	1st segment_base_hi	t_base_lo	1st buffer_point
	end_page=0	transfer_length=2nd segment_length	2nd scgment_length	2nd segment_base_lo	2nd buffer_point	3rd segment_length	3rd segment_base_lo	3rd buffer_point	4th segment_length	4th segment_base_lo	4th buffer_point	1st segment_length	1st segment_base_lo	1st buffe
	-start_page=1		2nd s			3rd se			4th se			1st se		



				т			_		т		_		_
page_counter=4	transfer_length=0	1st segment_base_hi	ıt_base_lo	1st buffer_point=Logical Block Address	2nd segment_base_hi	2nd segment_base_lo	2nd buffer_point= 1st buffer_point+2nd segment_length	3rd segment_base_hi	nt_base_lo	3rd buffer_point= 2nd buffer_point+3rd segment_length	4th segment_base_hi	nt_base_lo	4th buffer_point= 3rd buffer_point+4th segment_length
age=1 end_page=0	transfer	1st segment_length	1st segment_base_lo	1st buffer_point=L	2nd segment_length	2nd segme	2nd buffer_point= 1st buff	3rd segment_length	3rd segment_base_lo	3rd buffer_point= 2nd buf	4th segment_length	4th segment_base_lo	4th buffer_point= 3rd buff
start_page=1													

FIG. 24

-	page=3	nd segment	ıgth	2nd segment_base_lo	2nd buffer_point	gth	3rd segment_base_lo	3rd buffer_point	gth	4th segment_base_lo	4th buffer_point	gth	1st segment_base_lo	
	start_page=1 end_page=3	transfer_length=2nd segment_length+3rd segment_length	2nd segment_length	2nd	2	3rd segment_length	3rd	6	4th segment_length	4th	7	1st segment_length	1st	

FIG. 25

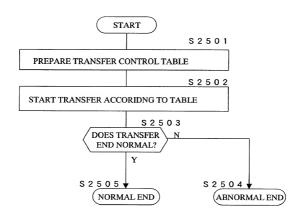


FIG. 27

708	2nd segment_base_hi	t_base_lo	buffer_length_1=2nd segment_length buffer_length_2=3rd segment_length	buffer_address_1 = 2nd buffer_point	buffer_address_2 = 3rd buffer_point
	buffer_number = 2	2nd segment_base_lo	buffer_length_1=2nd segment_length	buffer_address_1	buffer address 2

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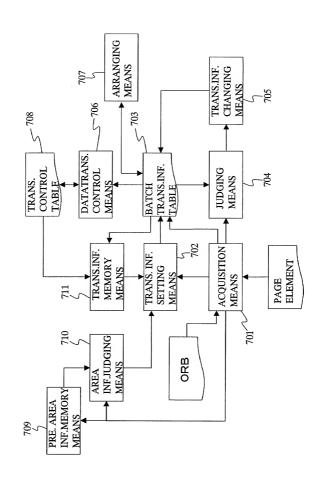
FIG. 28

703	page_counter=1	transfer_length=3rd segment_length	2nd segment_base_hi	2nd segment_base_lo	2nd buffer_point	3rd segment_base_hi	3rd segment_base_lo	3rd buffer_point	4th segment_base_hi	ıt_base_lo	4th buffer_point	1st segment_base_hi	ıt_base_lo	1st buffer_point
	• start_page=3 • end_page=4	transfer_length=3	2nd segment_length	2nd segme	2nd buf	3rd segment_length	3rd segme	3rd buff	4th segment_length	4th segment_base_lo	4th buff	1st segment_length	1st segment_base_lo	1st buff

FIG. 29

Host	1st	2nd	3rd	Last1	Last
CD9050	130	1	1		
CD9030			<u> </u>	1	1
186D000	14	15	15	15	15
186D000	13	14	14	14	14
186D000	15	16	16	16	16
1870000	12	13	13	13	13
1871000					
1872000					
1873000	11	12	12	12	12
1874000					
1875000	10	11	11	11	11
1876000	9	10	10	10	10
1877000					
1878000					***
1879000					
187A000					
187B000	6	7	7	7	7
187C000					
187D000					
187E000	5	6	6	6	6
187F000	7	8	8	8	8
1880000	4	5	5	5	5
1881000	8	9	9	9	9
1882000	3	4	4	4	4
1883000					
1884000	2	3	3	3	3
1885000					
1886000		2	2	2	2
1886500	1				
1893000	16				
.000000	101				

FIG. 30



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